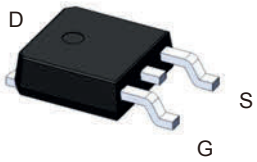
GOOD-ARK SEMICONDUCTOR

GSFD8003

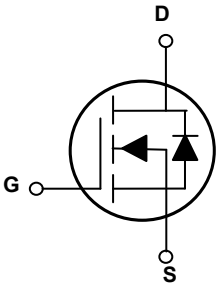
800V N-Channel MOSFET

Main Product Characteristics

$V_{(BR)DSS}$	800V
$R_{DS(ON)}$	4.8Ω (Max.)
I_D	3A



TO-252 (DPAK)



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery

Description

The GSFD8003 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	800	V
Gate-Source Voltage	V_{GS}	±30	V
Continuous Drain Current, @ Steady-State (T_C=25°C)	I_D	3	A
Continuous Drain Current, @ Steady-State (T_C=100°C)		1.9	
Pulsed Drain Current	I_{DM}	12	A
Power Dissipation (T_C=25°C)	P_D	80	W
		0.64	W/°C
Single Pulse Avalanche Energy[1]	E_{AS}	173	mJ
Junction-to-Ambient (PCB Mounted, Steady-State)	$R_{\theta JA}$	62	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.56	°C/W
Operating Junction Temperature Range	T_J	-55 To +150	°C
Storage Temperature Range	T_{STG}	-55 To +150	°C

Electrical Characteristics (T_C=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250uA	800	-	-	V
Drain-to-Source Leakage Current	I_{DSS}	V_{DS}=800V, V_{GS}=0V	-	-	1	μA
Gate-to-Source Forward Leakage	I_{GSS}	V_{DS}=0V, V_{GS}=±30V	-	-	±100	nA
Static Drain-to-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=1.5A	-	3.8	4.8	Ω
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS}=V_{DS}, I_D=250uA	2	-	4	V
Gate Resistance	R_g	F=1MHz	-	4.5	-	Ω
Dynamic and Switching Characteristics						
Total Gate Charge[2,3]	Q_g	V_{DD}=640V, I_D=3A, V_{GS}=10V	-	9	-	nC
Gate-Source Charge[2,3]	Q_{gs}		-	2.46	-	
Gate-to-Drain ("Miller") Charge[2,3]	Q_{gd}		-	3.74	-	
Turn-On Delay Time[2,3]	$t_{d(on)}$	V_{DD}=400V, R_G=25Ω, V_{GS}=10V, I_D=3A	-	13.87	-	nS
Rise Time[2,3]	t_r		-	30.53	-	
Turn-Off Delay Time[2,3]	$t_{d(off)}$		-	22.40	-	
Fall Time[2,3]	t_f		-	18.27	-	
Input Capacitance	C_{iss}	V_{DS}=25V, V_{GS}=0V, F=1MHz	-	390.5	-	pF
Output Capacitance	C_{oss}		-	42.5	-	
Reverse Transfer Capacitance	C_{rss}		-	2	-	
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Source Current (Body Diode)	I_S	T_C=25°C, MOSFET symbol showing the integral reverse p-n junction diode.	-	-	3	A
Source Pulse Current	I_{SM}		-	-	12	A
Diode Forward Voltage	V_{SD}	V_{GS}=0V, I_S=3A	-	-	1.4	V
Reverse Recovery Time	T_{rr}	I_F=3A, V_{GS}=0V, dI_F/dt=100A/μs	-	437	-	nS
Reverse Recovery Charge	Q_{rr}		-	1.68	-	uC

Note:

1. L=30mH, I_{AS}=3.15A, V_{DD}=100V, R_G=25Ω, starting temperature T_J=25°C.
2. Pulse test: Pulse width ≤ 300us, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

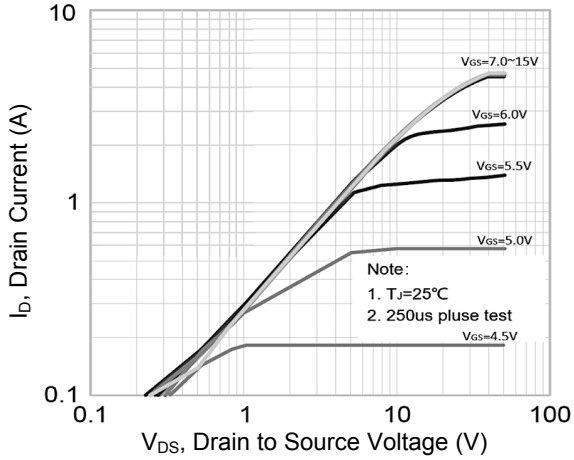


Figure 1. Typical Output Characteristics

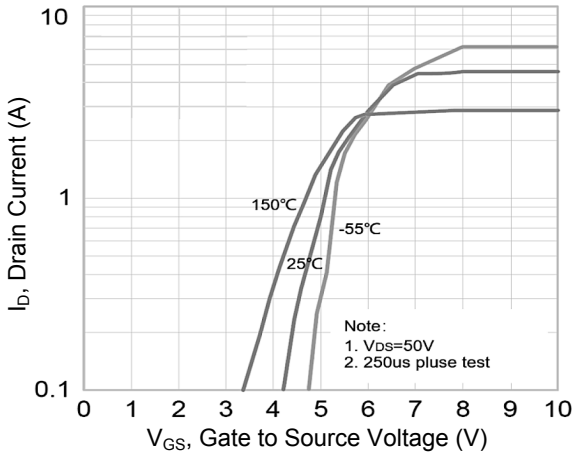


Figure 2. Transfer Characteristics

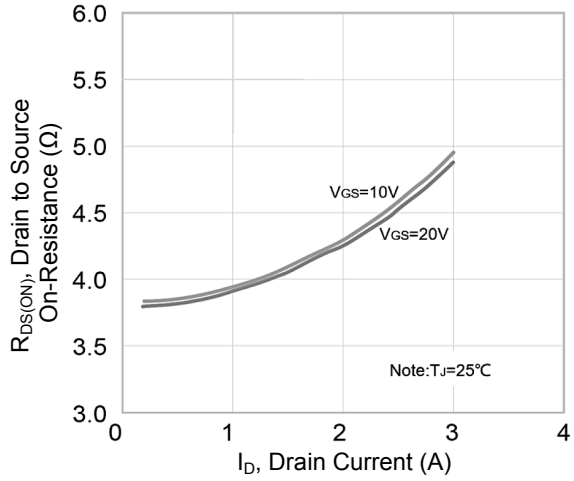


Figure 3. $R_{DS(ON)}$ vs. Drain Current

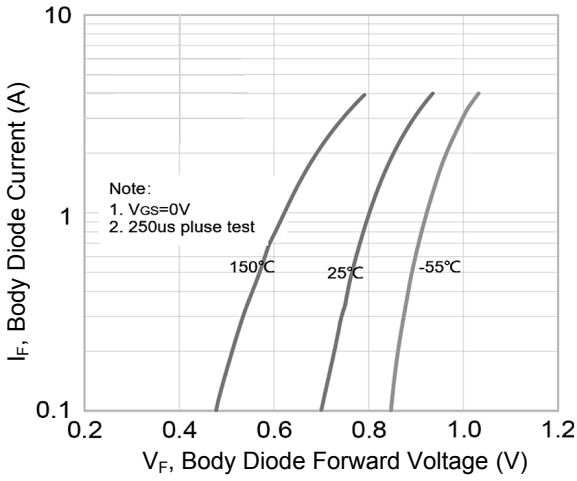


Figure 4. Body Diode Characteristic

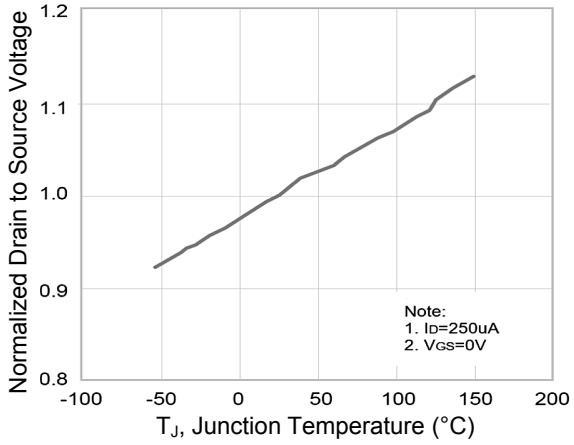


Figure 5. Normalized BV_{DSS} vs. T_J

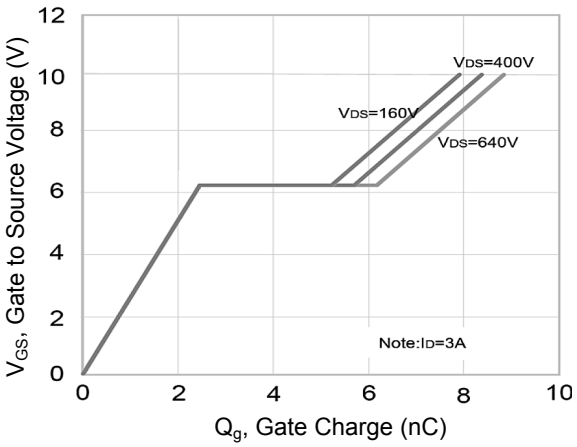


Figure 6. Gate Charge

Typical Electrical and Thermal Characteristic Curves

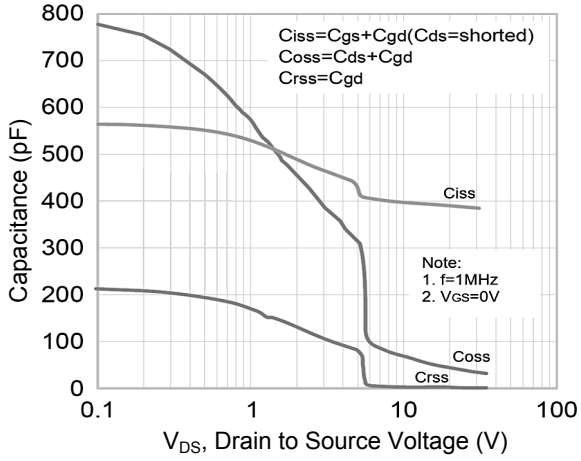


Figure 7. Capacitance Characteristic

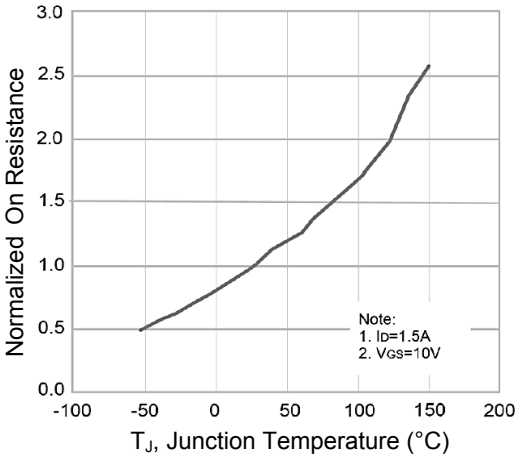


Figure 8. Normalized $R_{DS(ON)}$ vs. T_J

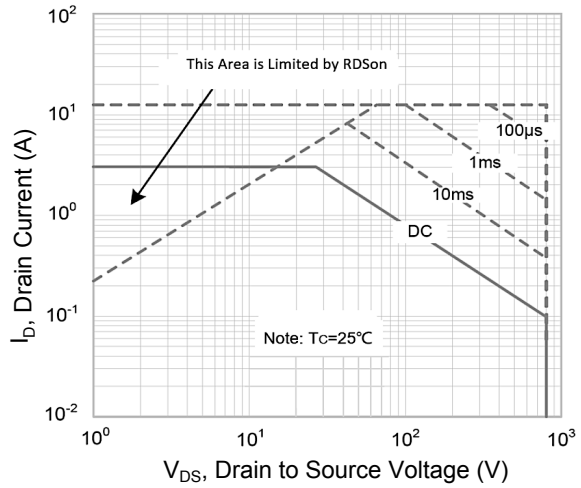
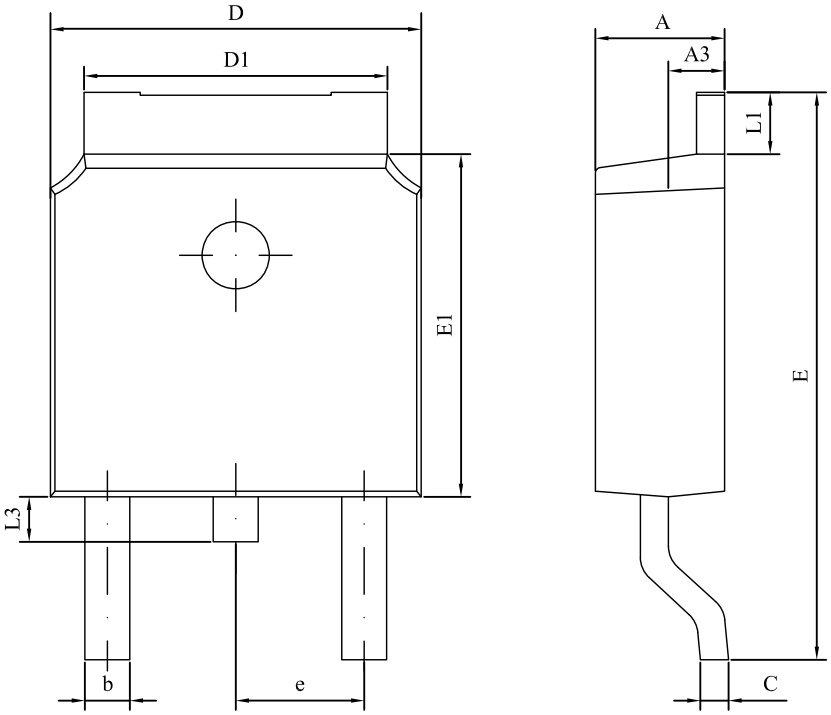


Figure 9. Safe Operation Area

Package Outline Dimensions TO-252 (DPAK)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.15	2.40	0.085	0.094
A3	0.90	1.10	0.035	0.043
b	0.50	0.90	0.020	0.035
C	0.40	0.65	0.016	0.026
D	6.30	6.90	0.248	0.272
D1	4.95	5.50	0.195	0.217
E	9.40	10.41	0.370	0.410
E1	5.90	6.30	0.232	0.248
e	2.286 BSC		0.090 BSC	
L1	0.89	1.27	0.035	0.050
L3	0.60	1.10	0.024	0.043

Order Information

Device	Package	Marking	Carrier	Quantity
GSFD8003	TO-252 (DPAK)	D8003	Tape & Reel	2,500 Pcs / Reel