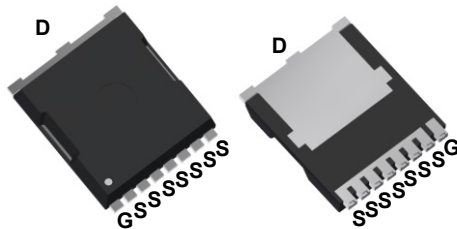
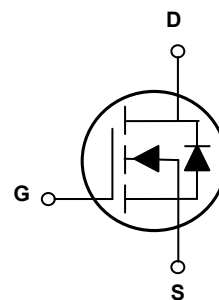


Main Product Characteristics

BV_{DSS}	100V
$R_{DS(ON)}$	1.5m Ω
I_D	300A



TOLL



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSGTL1R510 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_A=25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ($T_C=25^{\circ}C$)	I_D	300	A
Drain Current-Continuous ($T_C=100^{\circ}C$)		192	
Drain Current-Pulsed ¹	I_{DM}	1200	A
Single Pulse Avalanche Energy ²	E_{AS}	3120	mJ
Single Pulse Avalanche Current ²	I_{AS}	79	A
Power Dissipation ($T_C=25^{\circ}C$)	P_D	480	W
Power Dissipation-Derate above 25 $^{\circ}C$		3.2	
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62	$^{\circ}C/W$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.31	$^{\circ}C/W$
Operating Junction Temperature Range	T_J	-55 To +175	$^{\circ}C$
Storage Temperature Range	T_{STG}	-55 To +175	$^{\circ}C$

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=80V, V_{GS}=0V$ $T_J=25^\circ\text{C}$	-	-	1	μA
		$V_{DS}=80V, V_{GS}=0V$ $T_J=85^\circ\text{C}$	-	-	30	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=30A$	-	1.2	1.5	m Ω
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	2.0	2.8	4.0	V
Forward Transconductance	g_{fs}	$V_{DS}=10V, I_D=3A$	-	21	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{3,4}	Q_g	$V_{DS}=50V, I_D=100A,$ $V_{GS}=10V$	-	176	265	nC
Gate-Source Charge ^{3,4}	Q_{gs}		-	44	70	
Gate-Drain Charge ^{3,4}	Q_{gd}		-	48	75	
Turn-On Delay Time ^{3,4}	$t_{d(on)}$	$V_{DD}=50V, R_G=6\Omega,$ $V_{GS}=10V, I_D=100A$	-	35	55	nS
Rise Time ^{3,4}	t_r		-	50	75	
Turn-Off Delay Time ^{3,4}	$t_{d(off)}$		-	35	55	
Fall Time ^{3,4}	t_f		-	70	105	
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V,$ $F=1\text{MHz}$	-	10800	16200	pF
Output Capacitance	C_{oss}		-	3050	4580	
Reverse Transfer Capacitance	C_{rss}		-	35	55	
Gate Resistance	R_g	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	-	1.9	-	Ω
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Source Current	I_S	$V_G=V_D=0V, \text{Force Current}$	-	-	300	A
Pulsed Source Current	I_{SM}		-	-	600	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	-	-	1	V
Reverse Recovery Time	t_{rr}	$V_R=100V, I_S=10A,$ $di/dt=100A/\mu s, T_J=25^\circ\text{C}$	-	400	-	nS
Reverse Recovery Charge	Q_{rr}		-	1.6	-	μC

Note:

1. Repetitive rating: Pulsed width limited by maximum junction temperature.
2. $V_{DD}=50V, V_{GS}=10V, L=1\text{mH}, I_{AS}=79A, R_G=25\Omega,$ starting $T_J=25^\circ\text{C}$.
3. Pulsed test: pulse width $\leq 300\mu s,$ duty cycle $\leq 2\%$.
4. Essentially independent of operation temperature.

Typical Electrical and Thermal Characteristic Curves

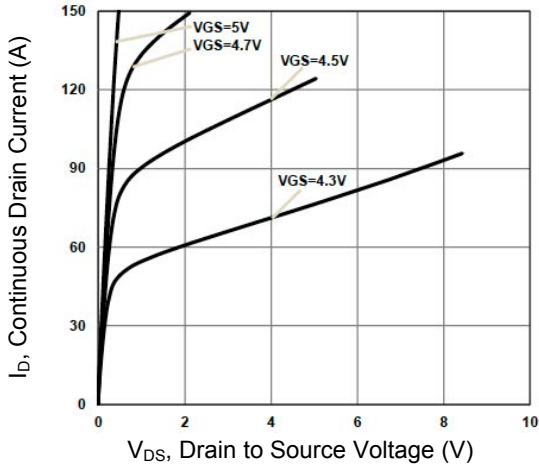


Figure 1. Typical Output Characteristics

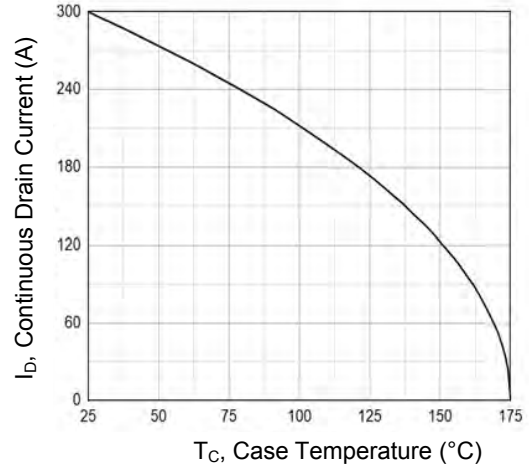


Figure 2. Continuous Drain Current vs. T_C

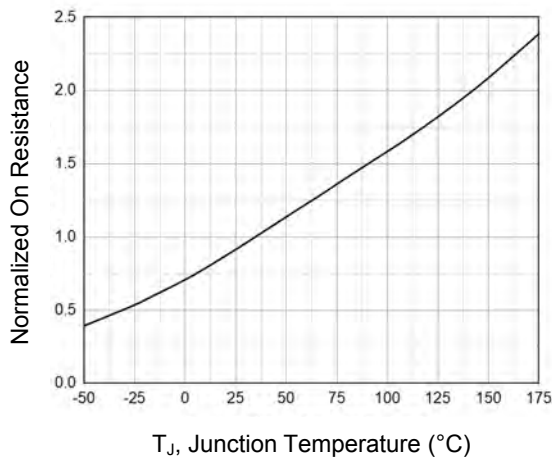


Figure 3. Normalized $R_{DS(on)}$ vs. T_J

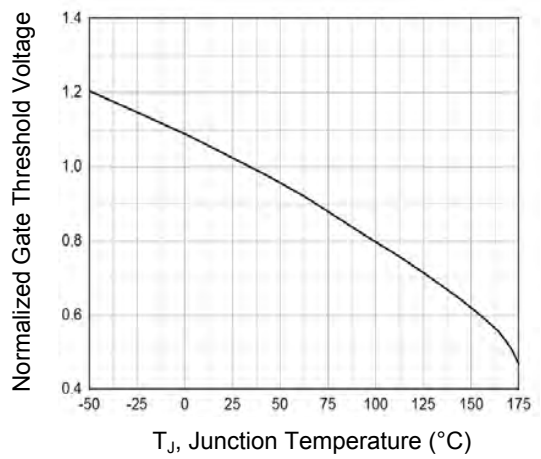


Figure 4. Normalized V_{th} vs. T_J

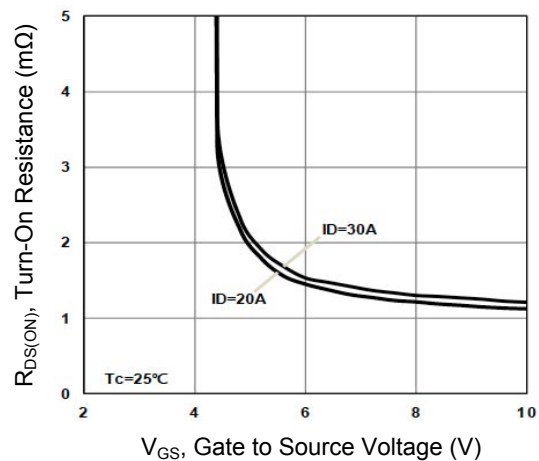


Figure 5. Turn-On Resistance vs. V_{GS}

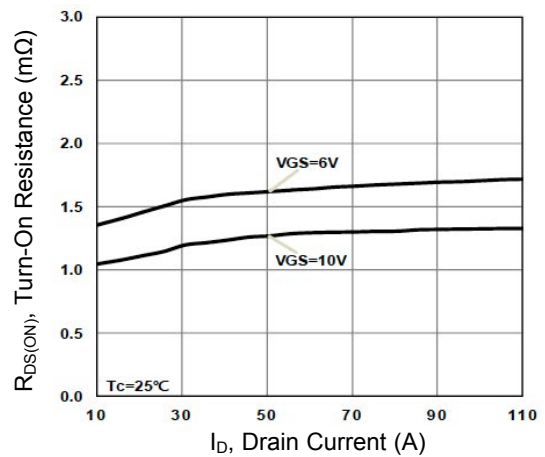


Figure 6. Turn-On Resistance vs. I_D

Typical Electrical and Thermal Characteristic Curves

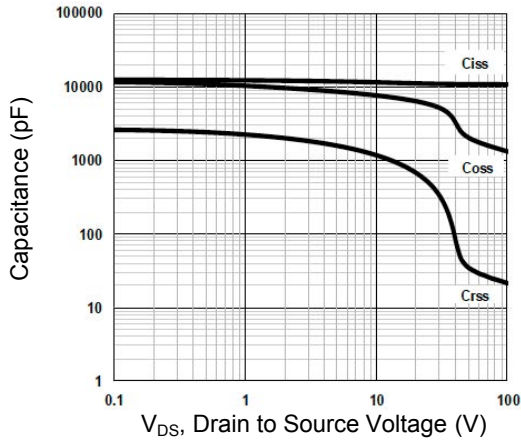


Figure 7. Capacitance Characteristics

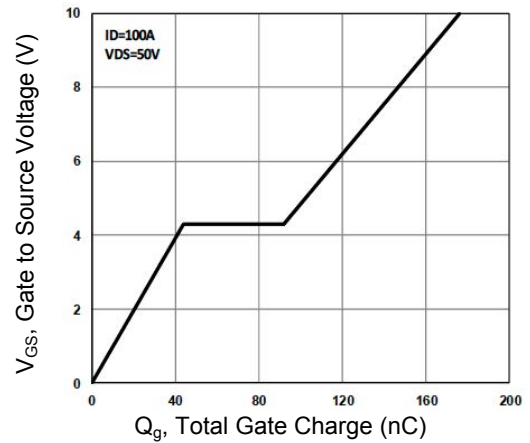


Figure 8. Gate Charge Characteristics

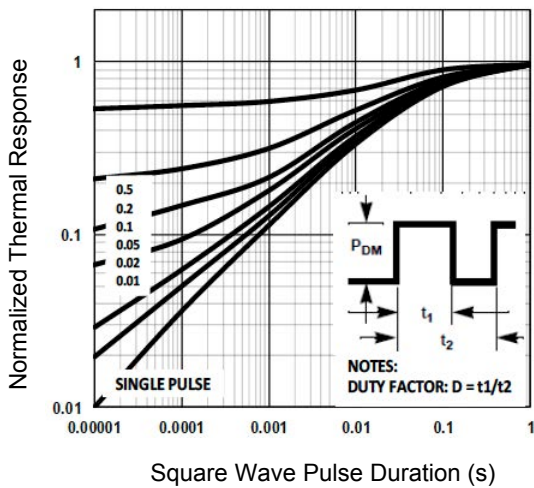


Figure 9. Normalized Transient Impedance

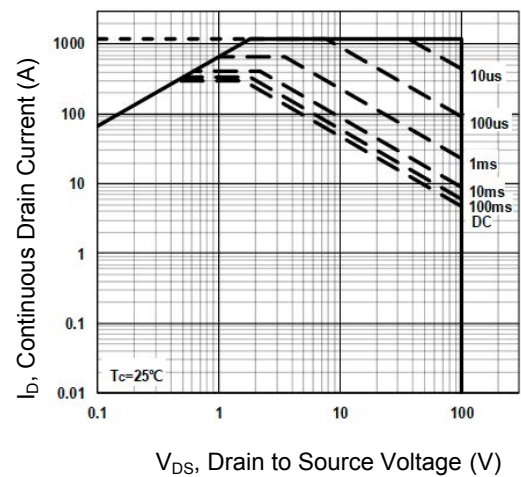
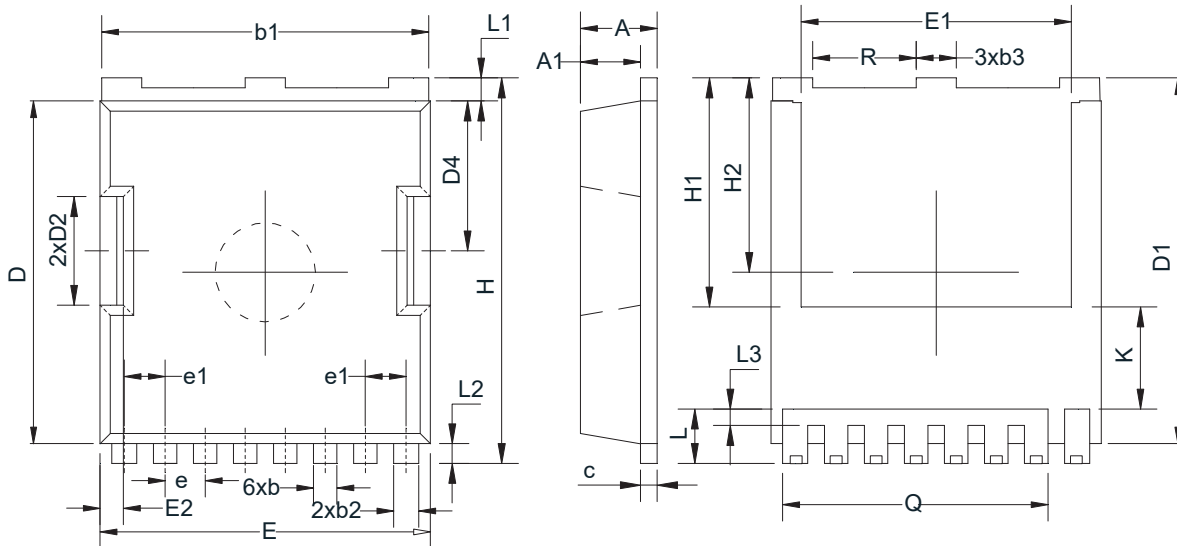


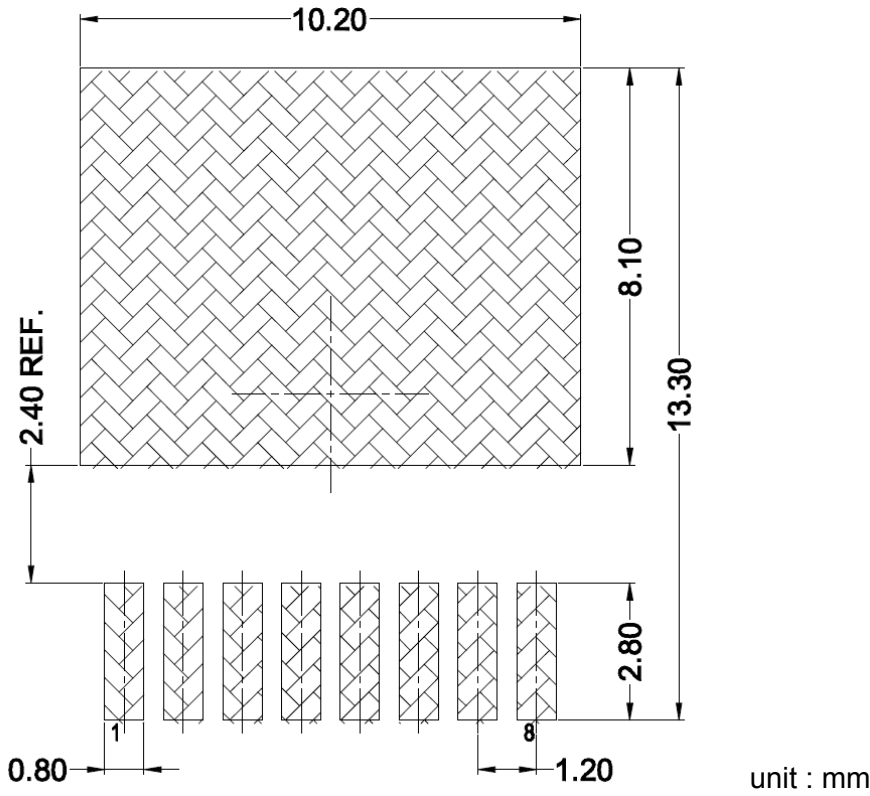
Figure 10. Maximum Safe Operation Area

Package Outline Dimensions (TOLL)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.25	2.35	0.089	0.093
A1	1.75	1.85	0.069	0.073
b	0.65	0.75	0.026	0.030
b1	9.75	9.85	0.384	0.388
b2	0.70	0.80	0.028	0.031
b3	1.15	1.25	0.045	0.049
c	0.45	0.55	0.018	0.022
D	10.35	10.45	0.407	0.411
D1	11.00	11.20	0.433	0.441
D2	3.25	3.35	0.128	0.132
D4	4.50	4.60	0.177	0.181
e	1.200 BSC		0.047 BSC	
e1	1.225 BSC		0.048 BSC	
E	9.85	9.95	0.388	0.392
E1	8.00	8.20	0.315	0.323
E2	0.65	0.75	0.026	0.030
H	11.60	11.80	0.457	0.465
H1	6.95 BSC		0.274 BSC	
H2	5.90 BSC		0.232 BSC	
K	3.10 REF		0.122 REF	
L	1.55	1.75	0.061	0.069
L1	0.65	0.75	0.026	0.030
L2	0.50	0.70	0.020	0.028
L3	0.40	0.60	0.016	0.024
Q	7.95 REF		0.313 REF	
R	3.05	3.15	0.120	0.124

Recommended Pad Layout



Order Information

Device	Package	Marking	Carrier	Quantity
GSGTL1R510	TOLL	TL1R510	Tape & Reel	2,000 Pcs / Reel